DC / DC converter for LCDs

BP5319 / BP5319X

The BP5319 and BP5319X are DC / DC converters for supplying power to liquid crystal displays (LCDs) panels. These modules supply a negative voltage from power supply of 5V. They are available in a single in-line package as an upright (BP5319) or L-shaped lead (BP5319X) type.

Applications

LCD panels in copiers, facsimiles, personal computers, word processors, instruments, and other displays

Features

- 1) Accurate output voltage. (-24V±0.75V)
- 2) High conversion efficiency. (typically 75%)
- 3) The external resistor can change an output voltage.
- 4) Built-in protection circuit.
- 5) Built-in ON/OFF switch.
- 6) Compact and light.
- 7) Available as an upright or L-shaped lead type.

■ Absolute maximum ratings (Ta=25°C)

Parameter	Symbol	Limits	Unit
Input voltage	Vin	7	V
Output current	lo	30	mA
ON / OFF CTL voltage	Vctl	7	V
Operating temperature range	Topr	−10 ~ +60	°C
Storage temperature range	Tstg	-30~+85	°C

Pin descriptions

Pin No.	Pin name	Function
1	Со	Output smoothing capacitor connection pin; connect a low-impedance capacitor with a recommended capacitance of 47µF between this pin and GND.
2	Vоит	Output pin
3	Vref	Output voltage adjustment pin for contrast; output voltages is adjusted by connecting a resistor between pins 2 and 3 or pins 3 and 4.
4, 7	GND	Ground pin; pins 4 and 7 are internal connection.
8	VстL	Output ON / OFF control pin; output starts when the pin is LOW level or OPEN, and stops when the pin HIGH level.
9	Vin	Input pin; connect a low-impedance capacitor with a recommended capacitance of 100mF between this pin and GND.

● Electrical characteristics (Unless otherwise noted, Ta=25°C)

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions
Input voltage	Vin	4.5	-	5.5	V	
Output current	Іоит	-	-	30	mA	
Output voltage	Vоит	-24.75	-24.00	-23.25	V	VIN=5V, IOUT=25mA
Line regulation	ΔV1	_	_	0.24	V	VIN=4.5~5.5V, IOUT=25mA
Load regulation	ΔV2	-	-	0.24	V	VIN=5V, IOUT=0~25mA
Output voltage temperature coefficient	ΔVt	-	-10	_	mV / °C	VIN=5V, IOUT=25mA *2
Ripple noise voltage	ν1	-	-	150	mV _{PP}	VIN=5V, IOUT=25mA *1
Conversion efficiency	η	70	75	_	%	VIN=5V, IOUT=25mA
ON / OFF CTL voltage when OFF	Vctl	2.0	_	_	V	Vin=5V
ON / OFF CTL voltage when ON	Vctl	-	-	0.5	V	Vin=5V
		(Alternatively, when Open)				
ON / OFF CTL input current	ICTL	_	100	150	μΑ	VIN=4.5~5.5V, VCTL=5V *2
Current consumption when OFF	loff	_	_	0.5	mA	VIN=4.5~5.5V, VCTL=5V *2
R1 resistance	R1	50	-	∞	kΩ	VIN=4.5~5.5V, VCTL=5V *2
R2 resistance	R2	50	_	∞	kΩ	VIN=4.5~5.5V, VCTL=5V *2

^{*1} Measured with a bandwidth of 20MHz. *2 Ta=-10~60°C

● Measurement circuit BP5319 / BP5319X ON / OFF SW Vout C2 C3 TH VCTL VCTL P A T T T C1

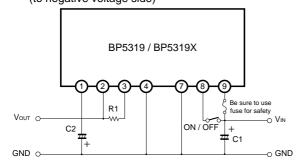
Fig.1

C1 : $100\mu F$ / 16V (Low-impedance capacitor) C2 : $47\mu F$ / 35V (Low-impedance capacitor) C3 : $0.022\mu F$ / 50V (Ceramic capacitor)

Application example

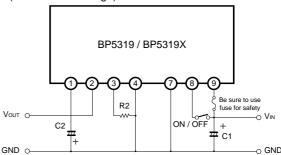
GND O

(1) When increasing the output voltage (to negative voltage side)



Note) Set up the change of the output voltage in the range of the territory(Fig.2) which can be used.

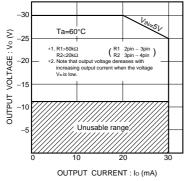
(2) When reducing the output voltage (near to zero voltage)



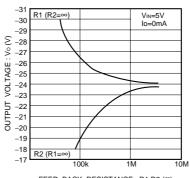
Operation notes

- (1) Place I/O external capacitors as near as possible to the connection pins. In particular, make sure to minimize the impedance between the input-side capacitor (C1) and pin 9. (Reference value: A length less than 50mm is recommended for a copper foil of 1.0mm wide and 35μ m thick.)
- (2) Avoid frequent switching using the ON / OFF CTL pin (5 times per second at the maximum).

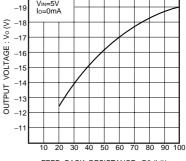
Electrical characteristic curves



TPUT CURRENT : Io (mA) FEED BA



FEED BACK RESISTANCE : R1,R2 (Ω)



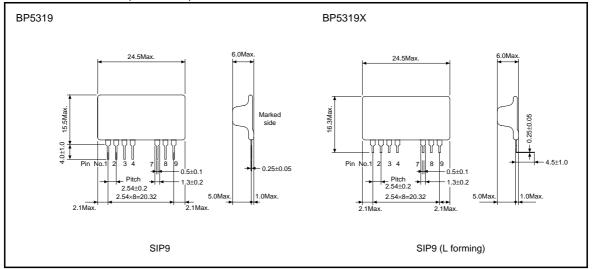
FEED BACK RESISTANCE : R2 ($k\Omega$)

Fig.2 Usable range

Fig.3 Output voltage vs. Feedback resistance (R1, R2)

Fig.4 Output voltage vs. Feedback resistance (R2<100kW)

● External dimensions (Units : mm)



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